

Fig. 1

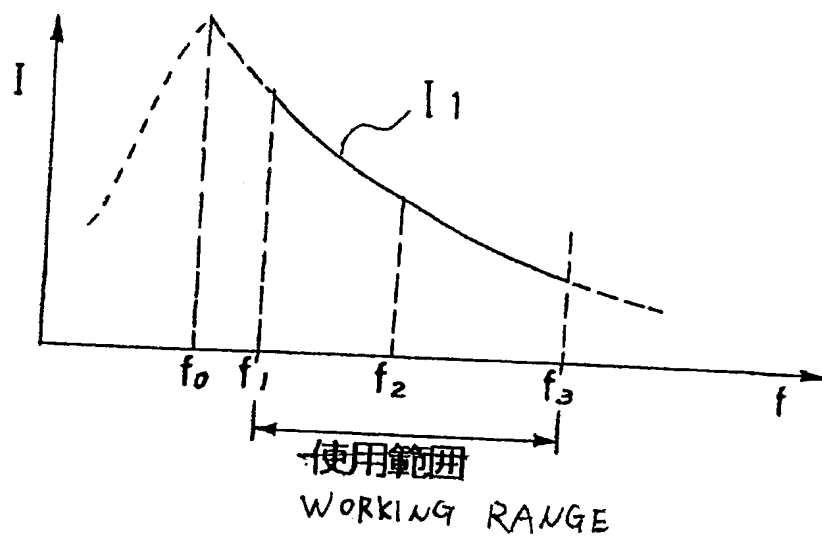


Fig 2

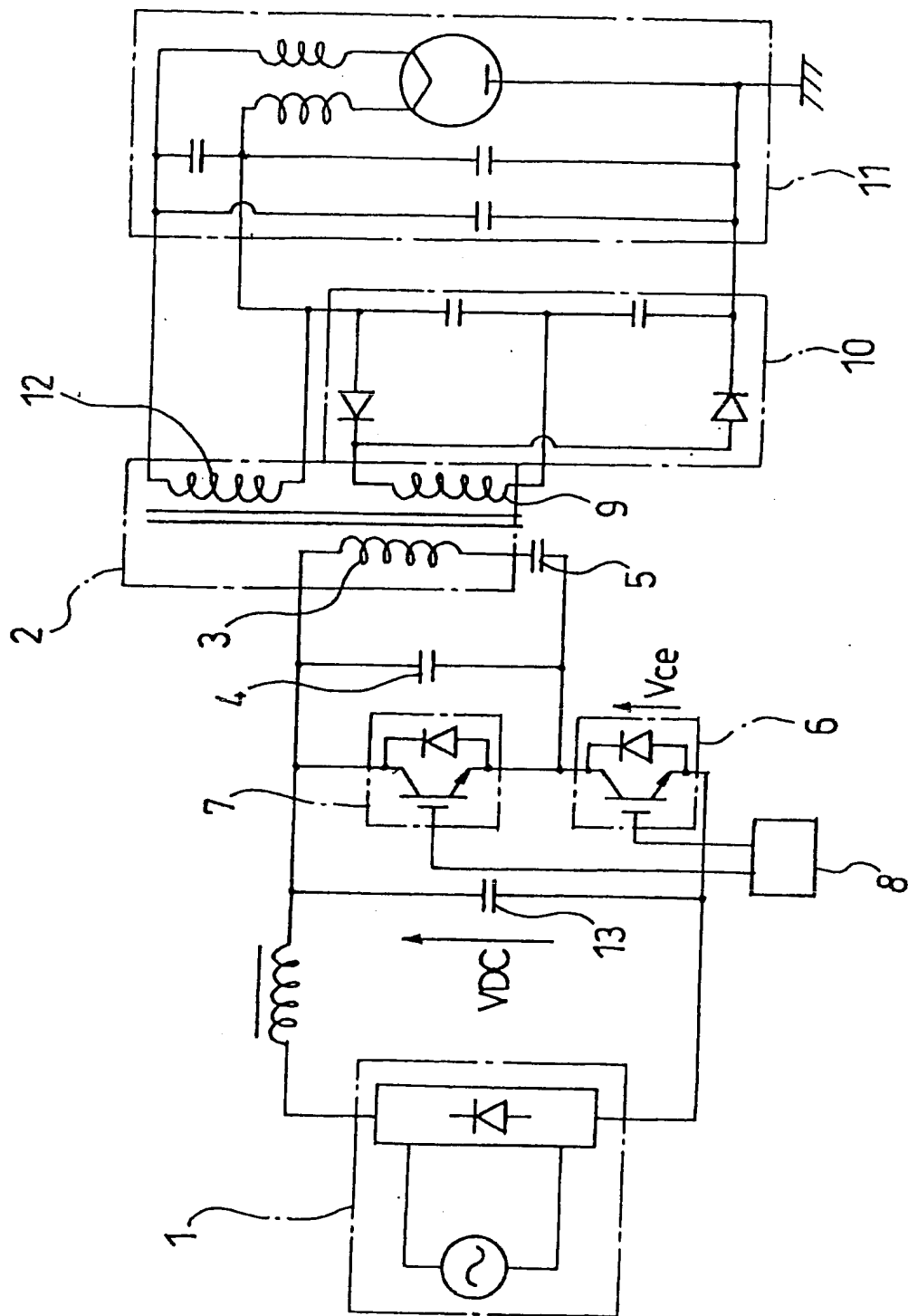
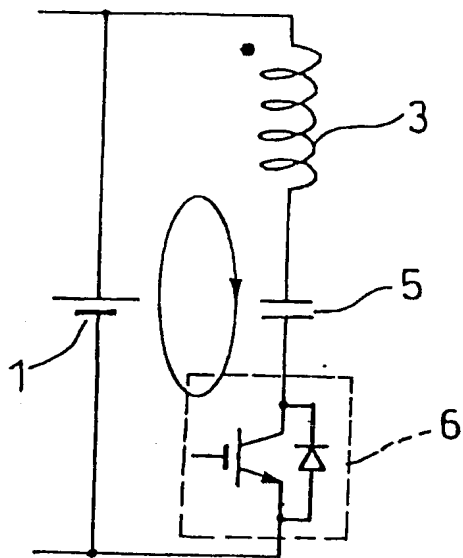
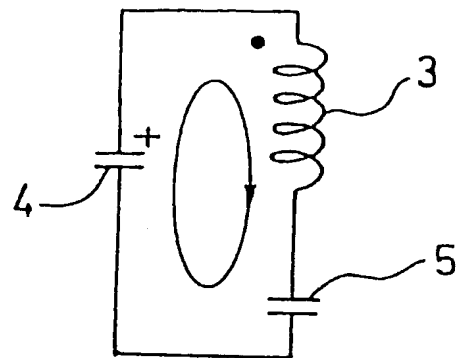


Fig. 3

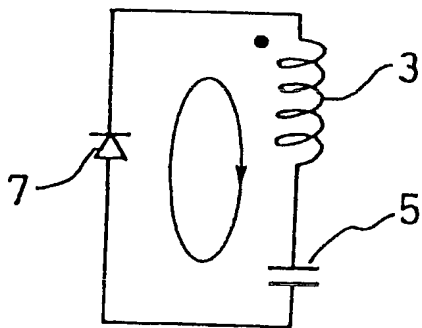
(a)



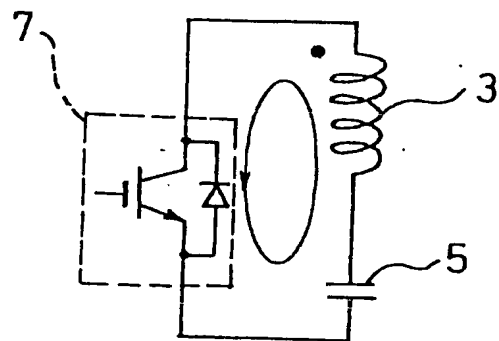
(b)



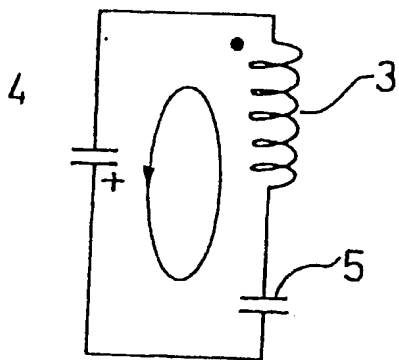
(c)



(d)



(e)



(f)

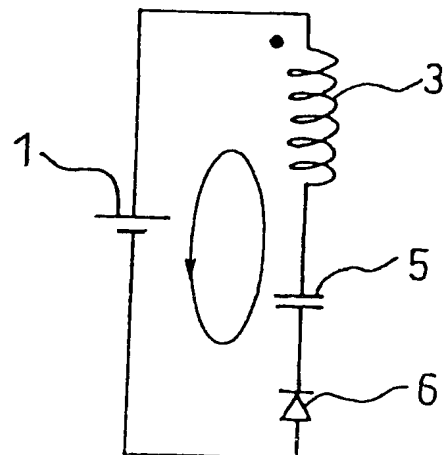


Fig. 4

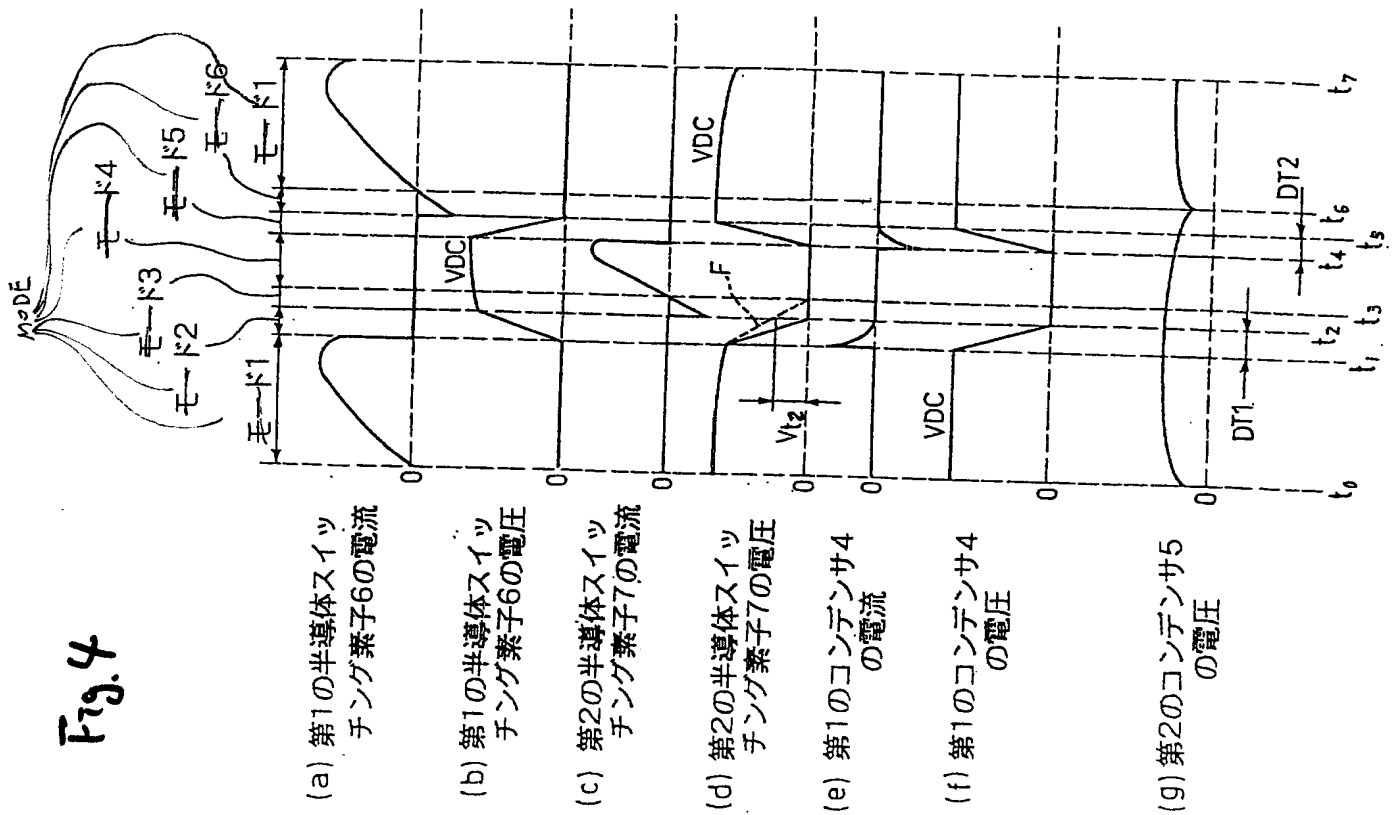
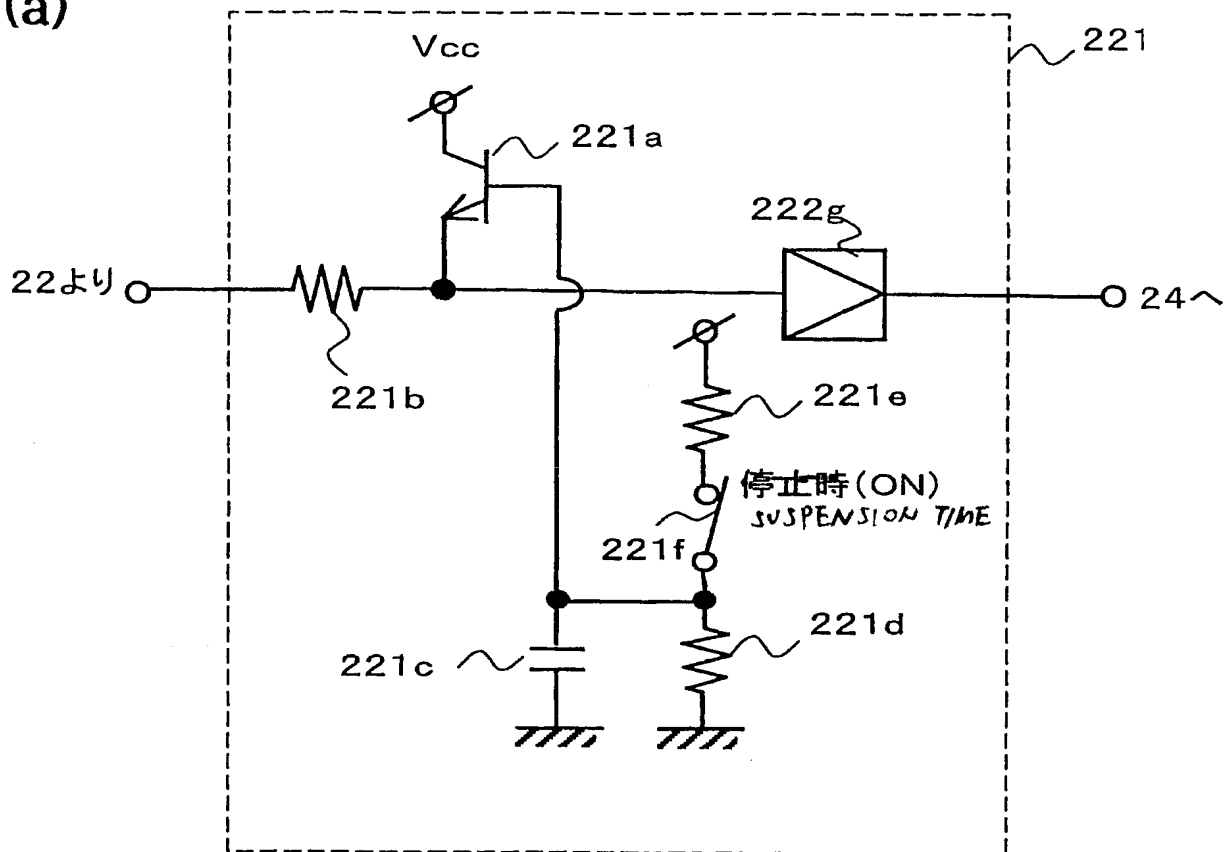


FIG. 4

- (a) 第1の半導体スイッチ
チング素子6の電流
- (b) 第1の半導体スイッチ
チング素子6の電圧
- (c) 第2の半導体スイッチ
チング素子7の電流
- (d) 第2の半導体スイッチ
チング素子7の電圧
- (e) 第1のコンデンサ4
の電流
- (f) 第1のコンデンサ4
の電圧
- (g) 第2のコンデンサ5
の電圧

Fig. 6

(a)



(b)

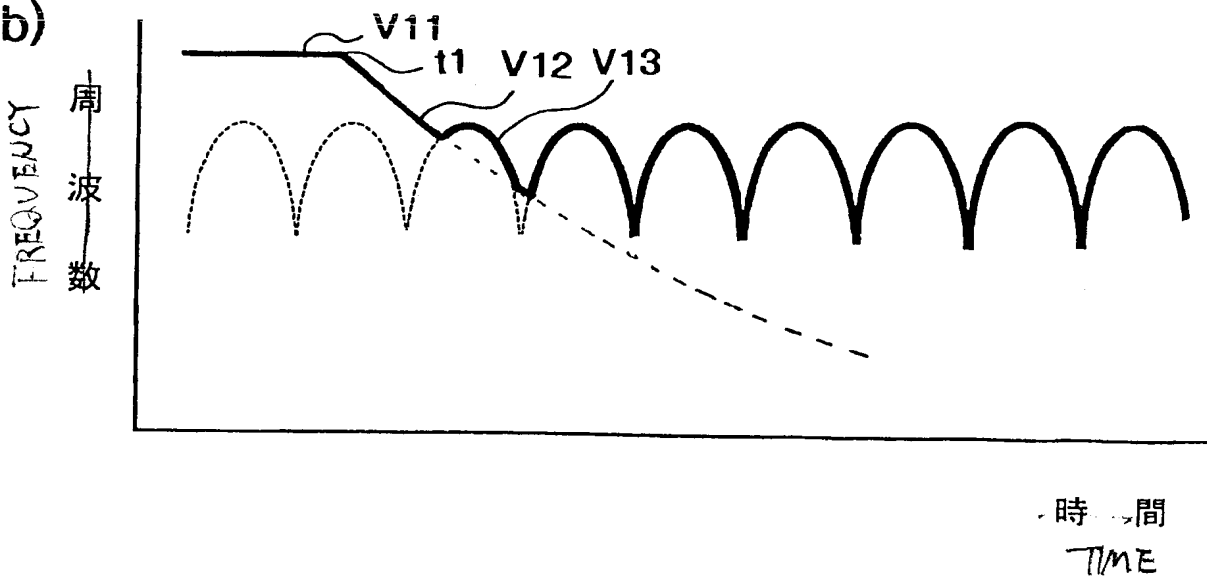
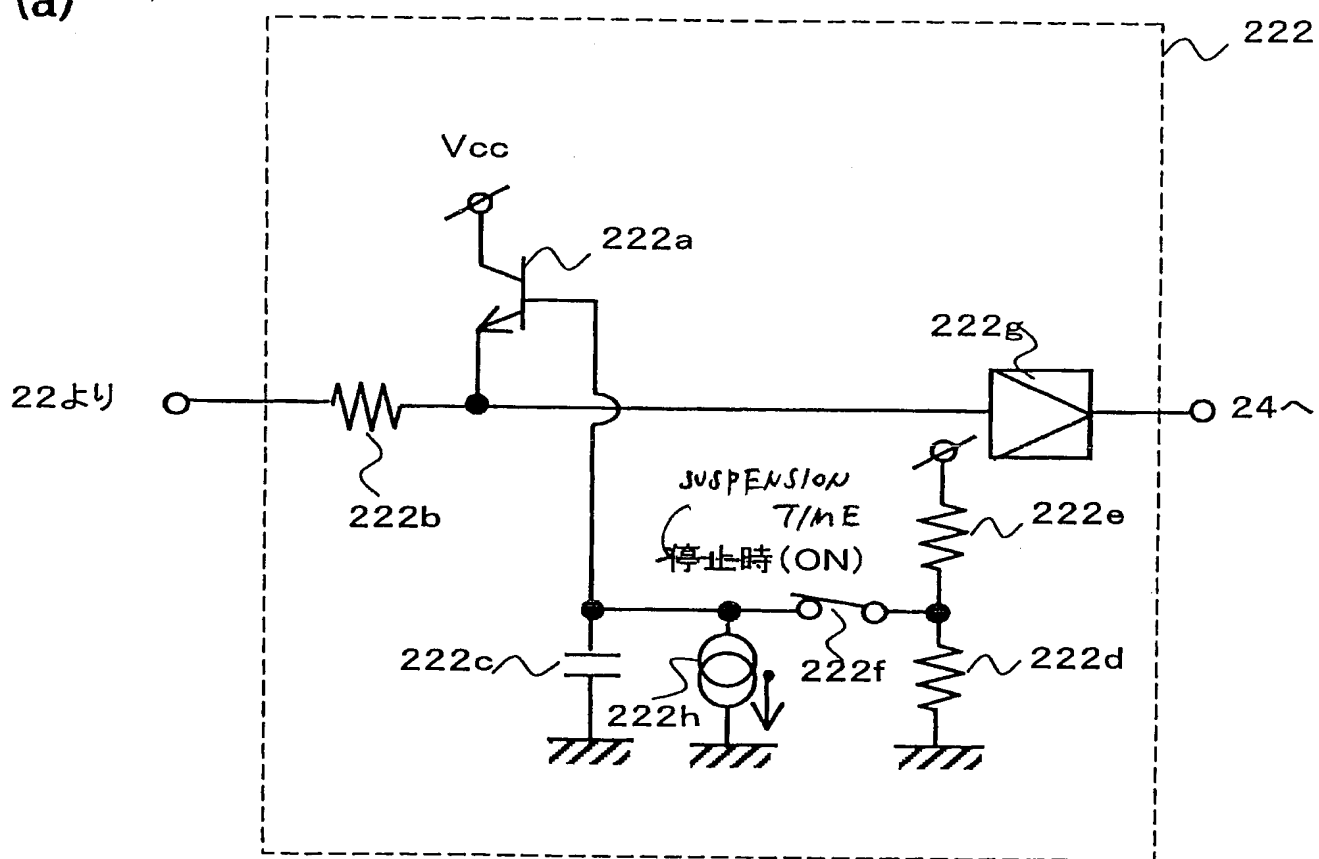


Fig. 7

(a)



(b)

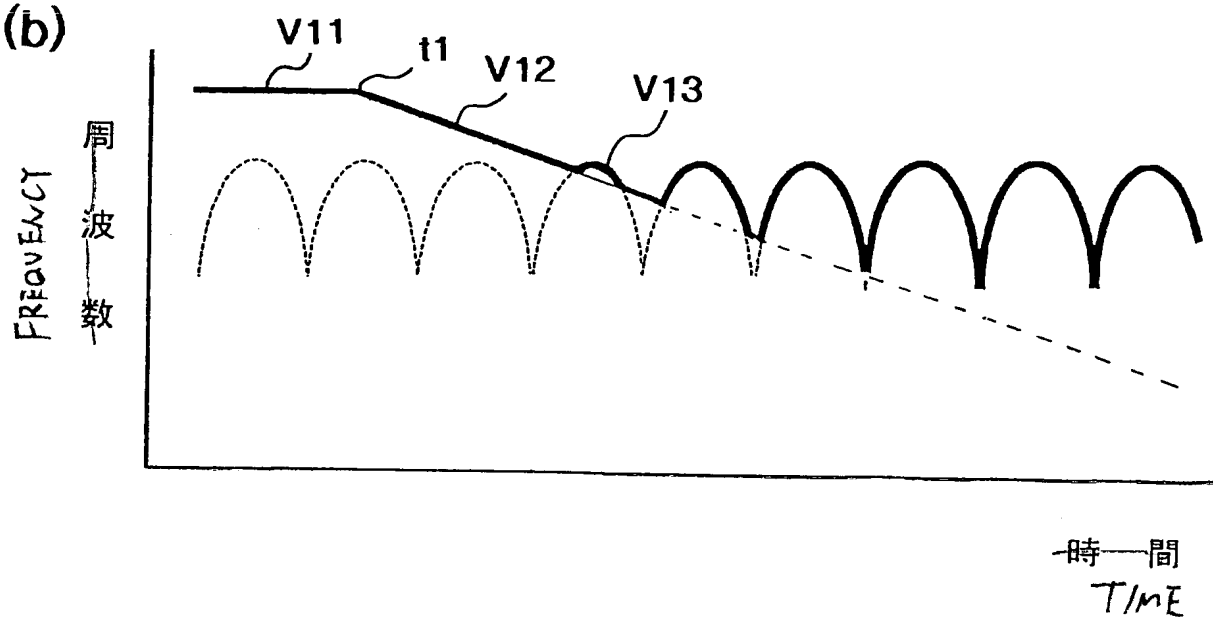
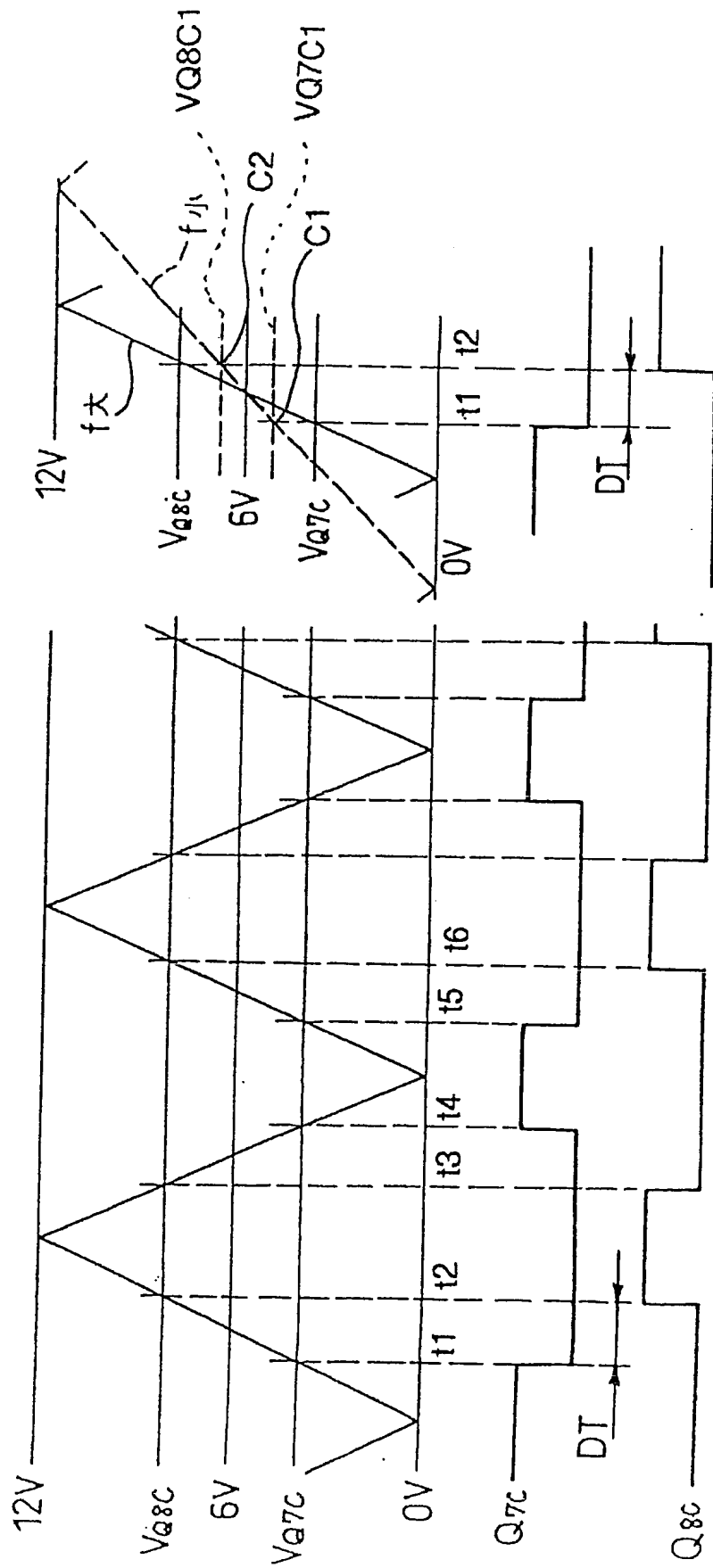


Fig. 8



(a)

(b)

Fig. 9

TO 23

DEAD TIME GENERATION CIRCUIT

23のa

24 デッドタイム作成回路

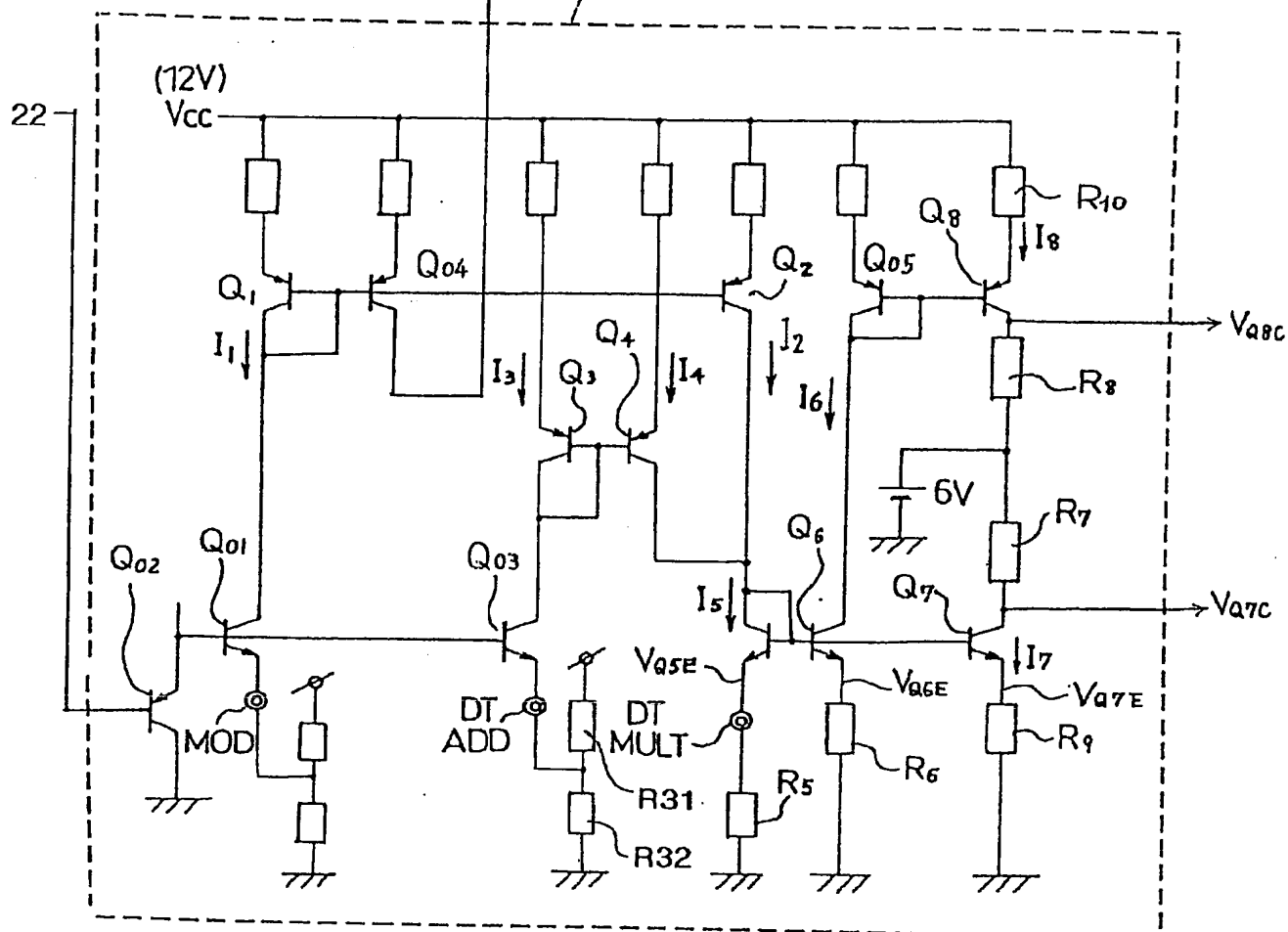


Fig. 10

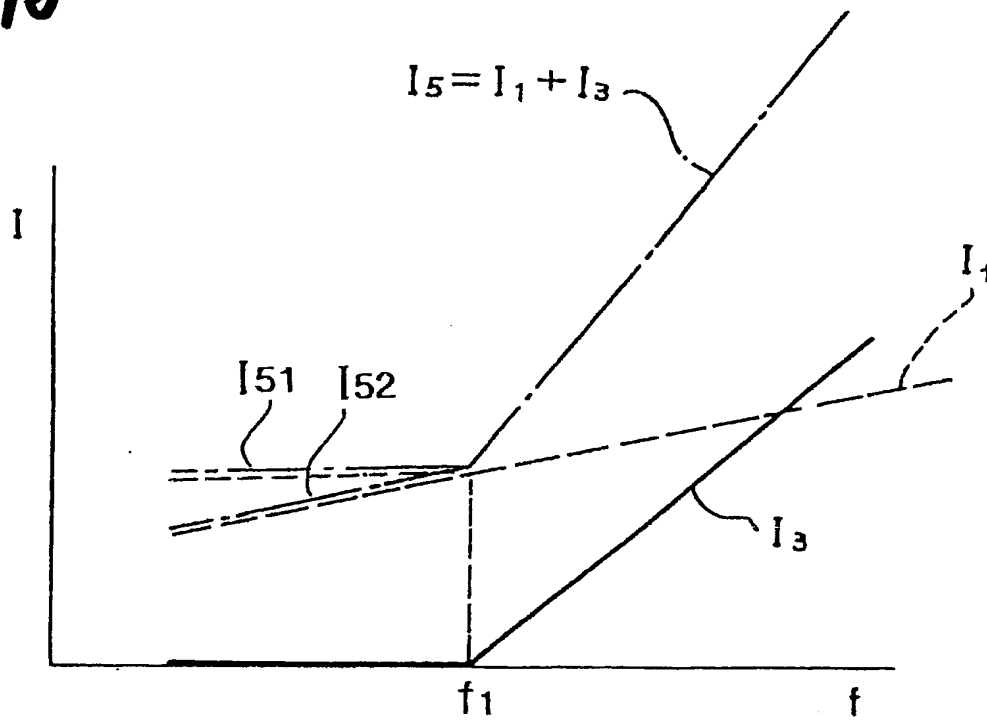


Fig. 11

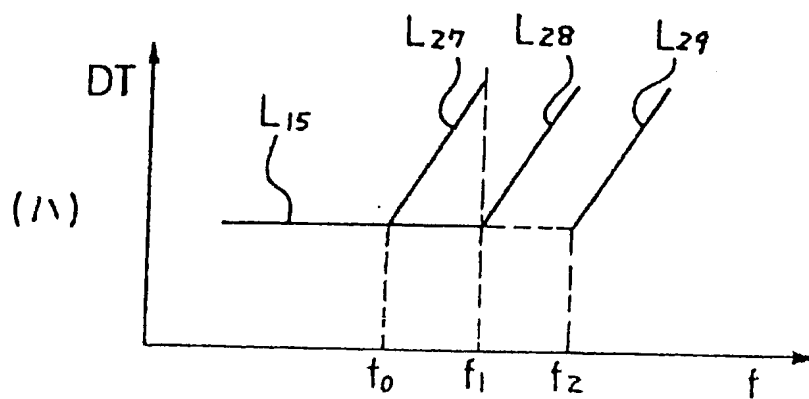
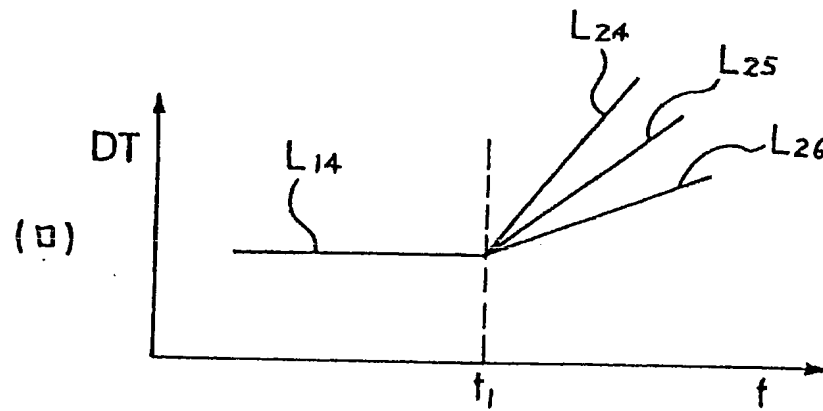
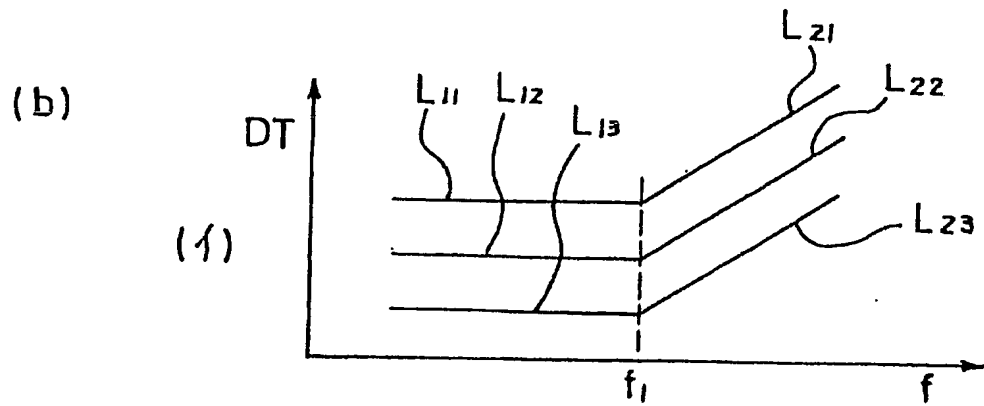
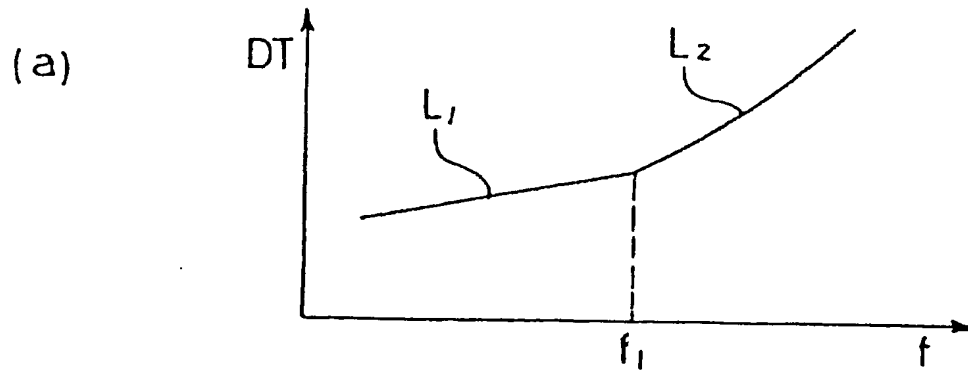
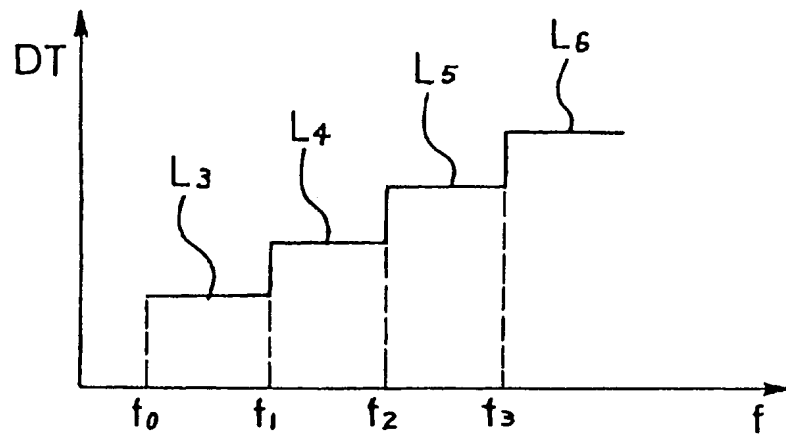


Fig. 12



23/

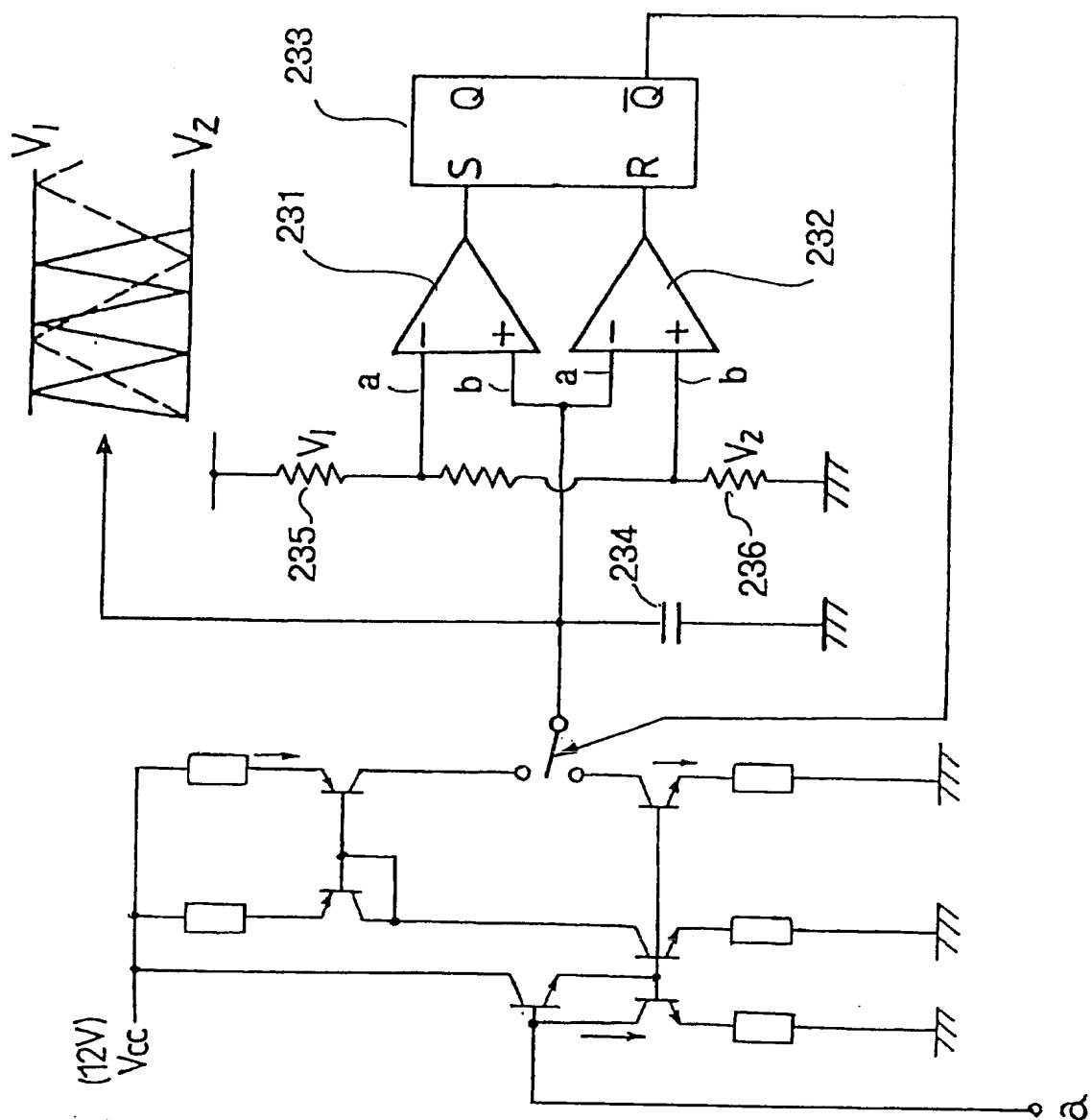
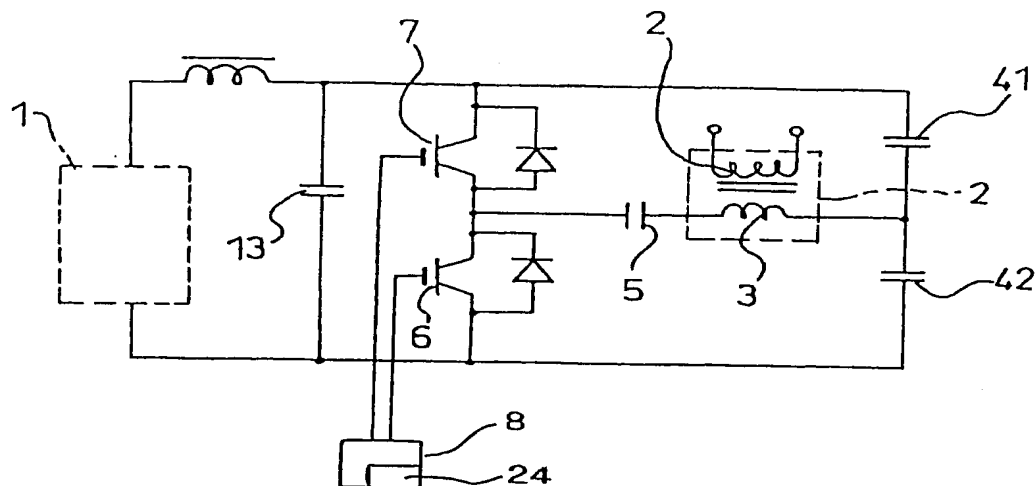
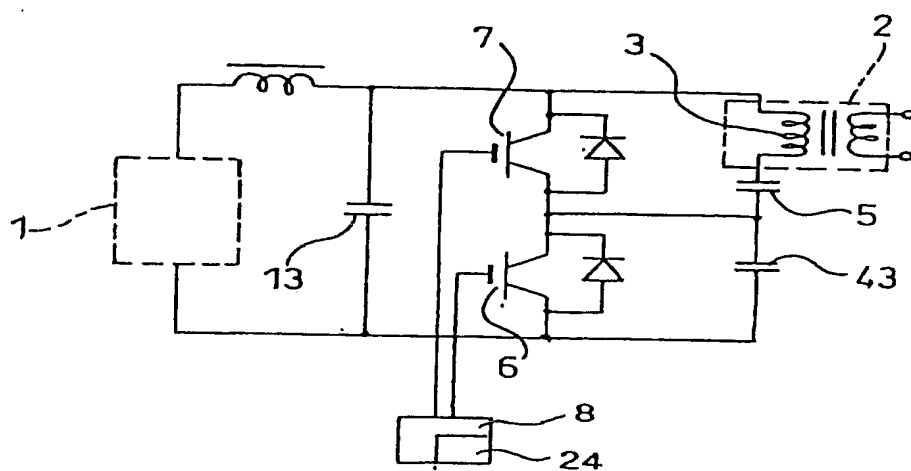


Fig. 14

(a)



(b)



(c)

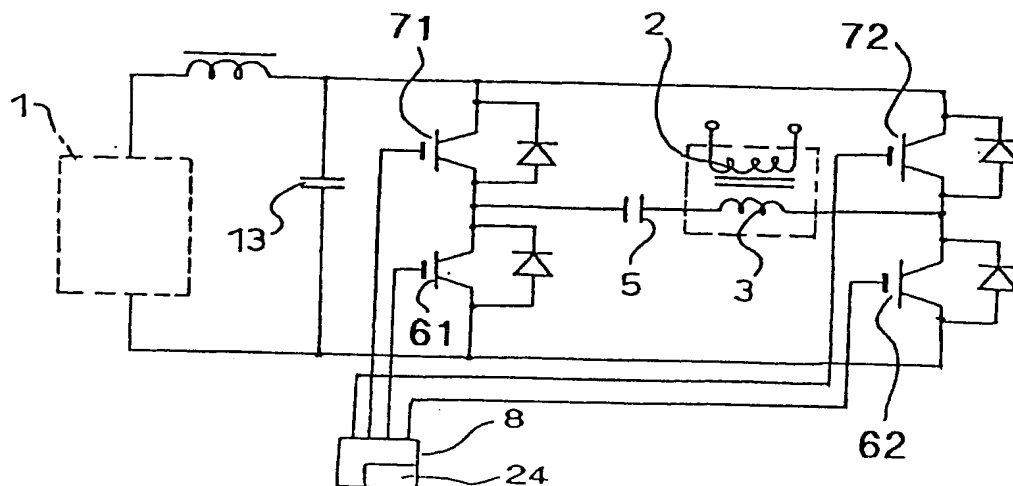


Fig. 15

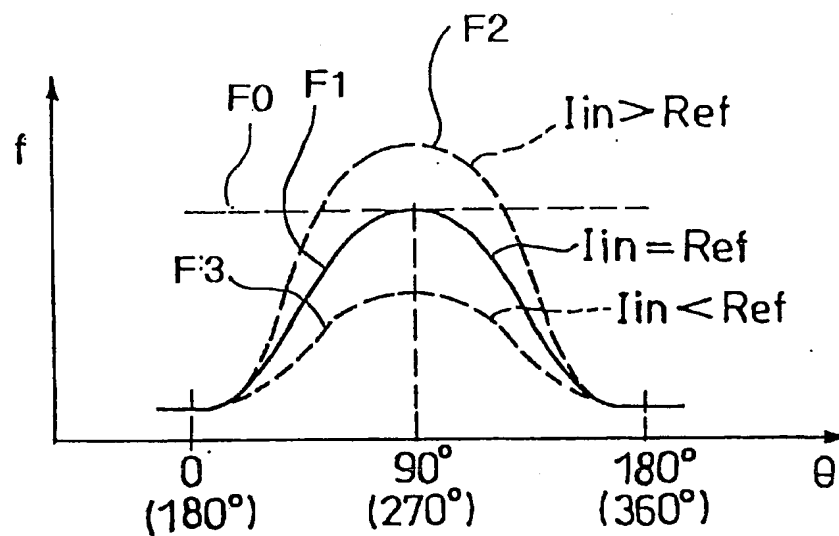


Fig. 16

